



IFW

XA-9947
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Nobuyuki SUGII et al.

Appln. No.: 10/673,789

Group Art Unit: 2814

Filed: September 30, 2004

Examiner: L. Pham

For: INSULATED-GATE FIELD-EFFECT TRANSISTOR, METHOD OF
FABRICATING THE SAME, AND SEMICONDUCTOR DEVICE
EMPLOYING SAME

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Responding to the Office Action mailed November 24, 2004, and to the requirement for restriction therein, Applicants elect invention II (Claims 1-18) for examination, reserving the right to file a divisional application with regard to the non-elected invention.

An action on the merits of the claims to the elected invention is respectfully requested.

The Examiner is requested to acknowledge the Preliminary Amendment dated November 10, 2004 in the next official communication.

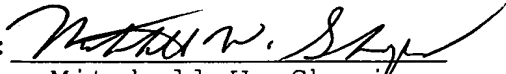
The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and

has not been requested separately, such extension is hereby requested.

Respectfully submitted,

MWS:lmb

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By: 
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Reg. No. 31,568

December 23, 2004